



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Toshiyuki TAKEWAKI, et al.

Application No.: 10/803,832

Filed: March 18, 2004

For: **SEMICONDUCTOR DEVICE AND  
METHOD FOR MANUFACTURING SAME:**

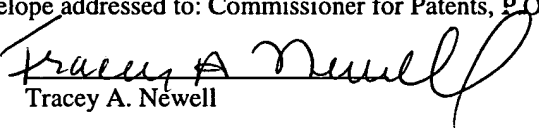
Art Unit: 2812

Examiner: GURLEY, Lynne Ann

Attorney Docket: PRM-00201

**CERTIFICATE OF MAILING**

I hereby certify that the foregoing document is being deposited with the United States Postal Service as first class mail, postage prepaid, "Post Office to Addressee", in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 18, 2004.

  
Tracey A. Newell

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

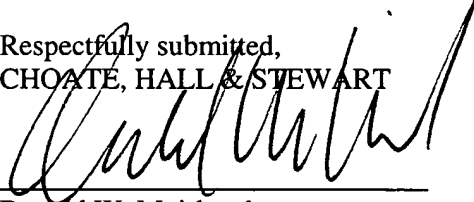
Sir:

In response to the Office Action dated July 27, 2004, in which restriction was required, Applicant elects Group II, claims 1-7, drawn to a semiconductor device, classified in class 257, subclass 734+.

Although we believe that we have appropriately provided for any fees due in connection with this submission, the Commissioner is hereby authorized to credit any overpayment or charge any deficiencies to/from our **Deposit Account No. 031721**. Two originally-executed copies of this form are being submitted.

Favorable consideration and allowance are earnestly solicited. Should there be any questions after reviewing this paper, the Examiner is invited to contact the undersigned at 617-248-4038.

Respectfully submitted,  
CHOATE, HALL & STEWART

  
Donald W. Muirhead  
Reg. No. 33,978

August 18, 2004

Date

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